

Fig. 1

CHIP TO CHIP INTERFACE

Robert Walker

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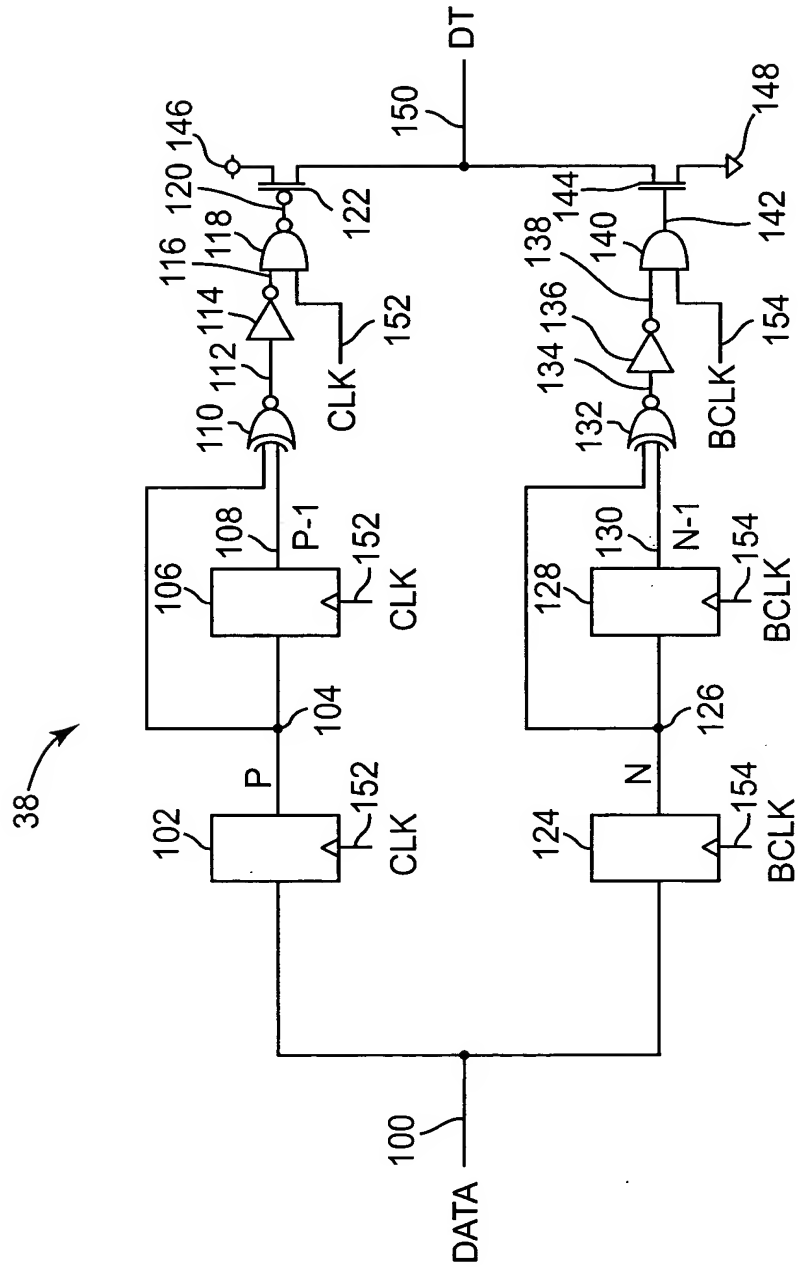
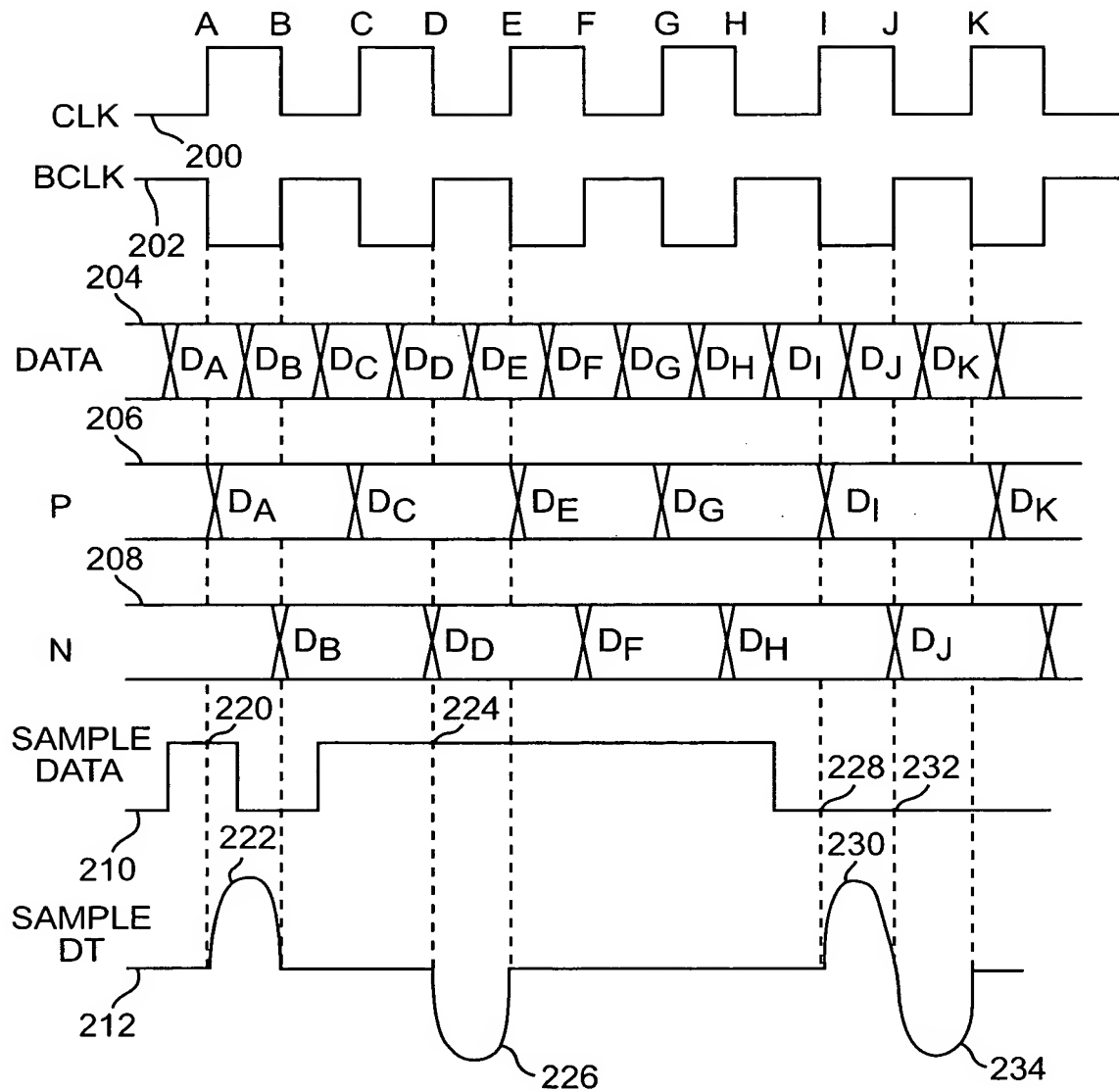


Fig. 2

CHIP TO CHIP INTERFACE

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**Fig. 3**

CHIP TO CHIP INTERFACE

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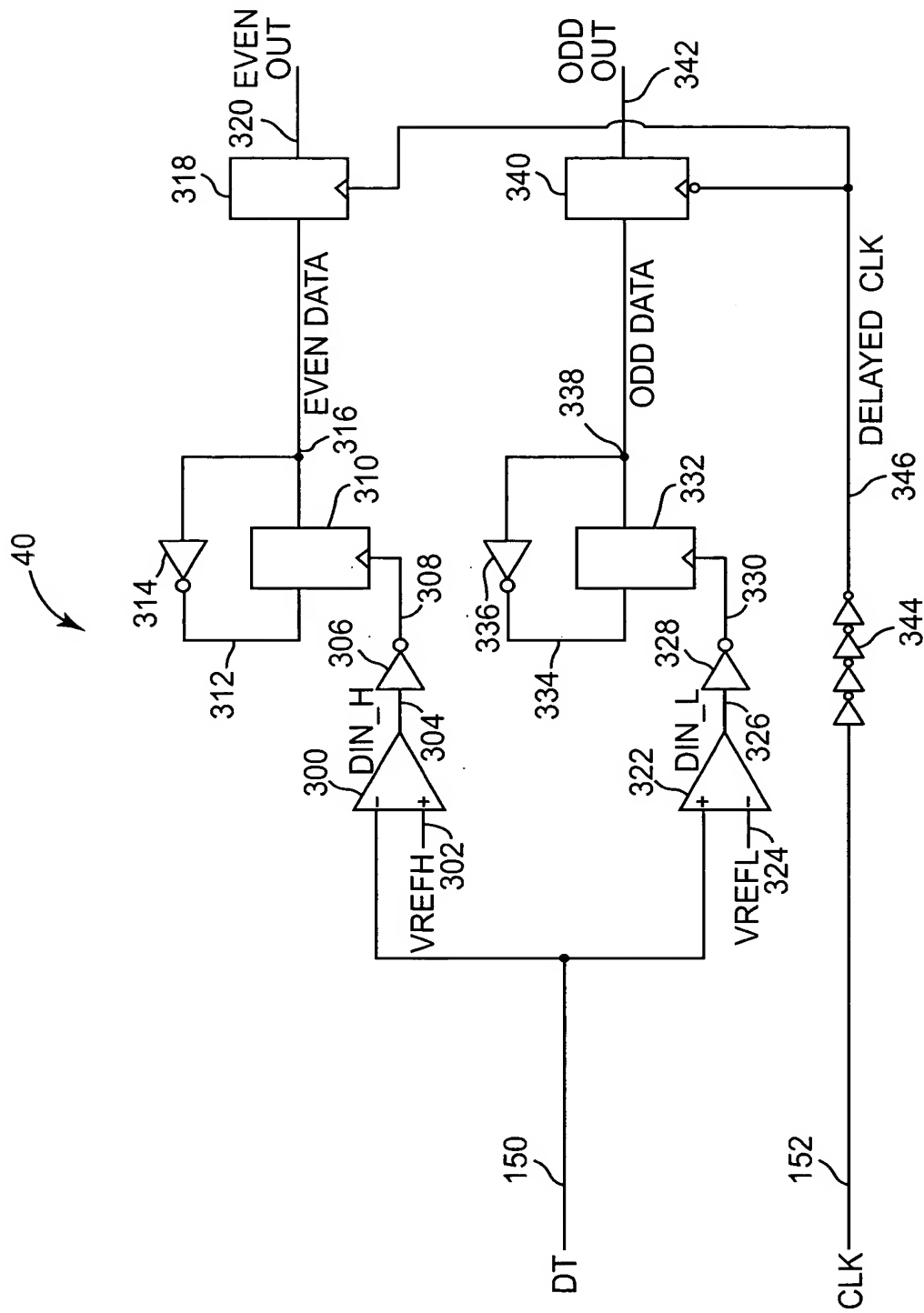


Fig. 4

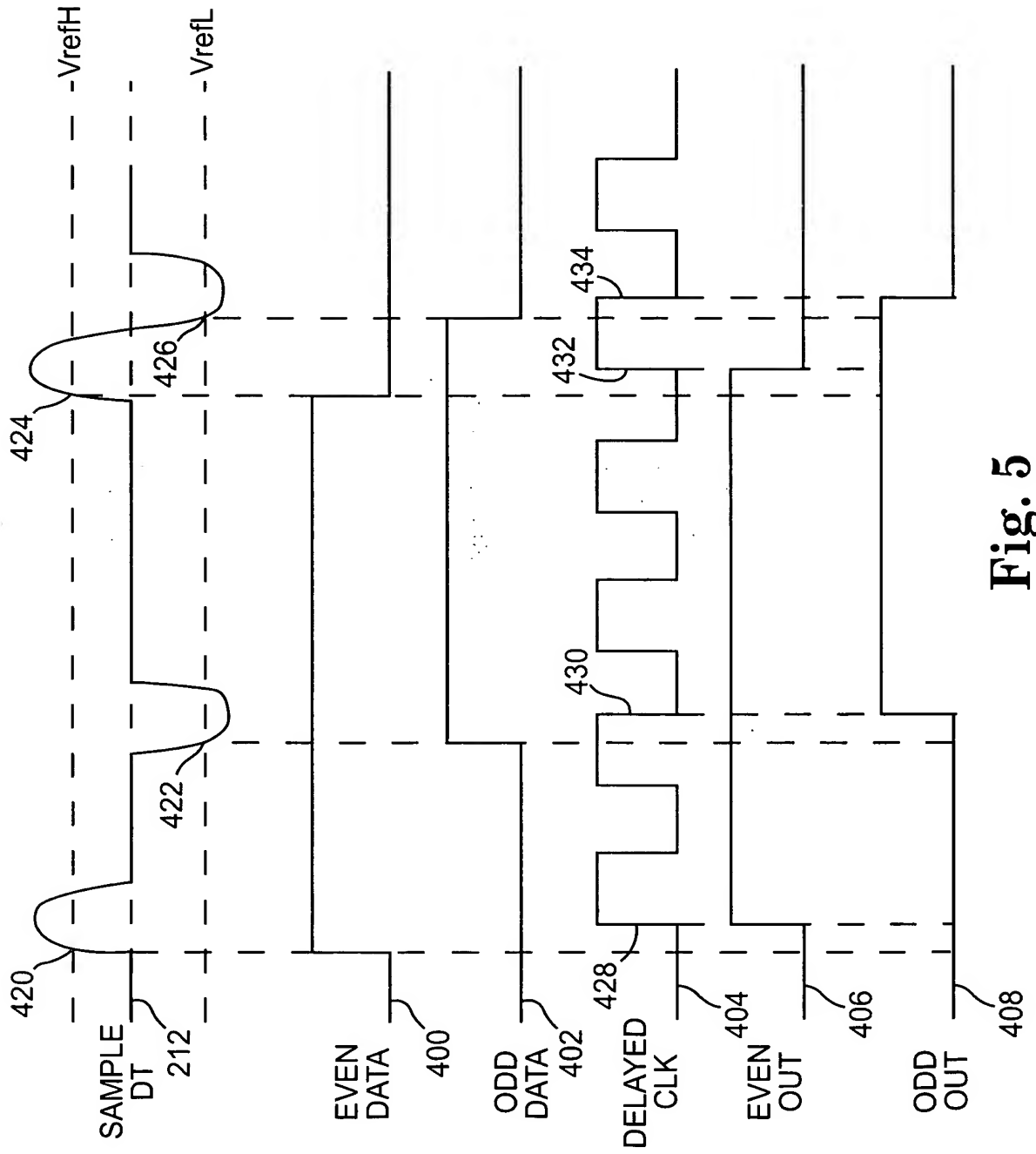


Fig. 5

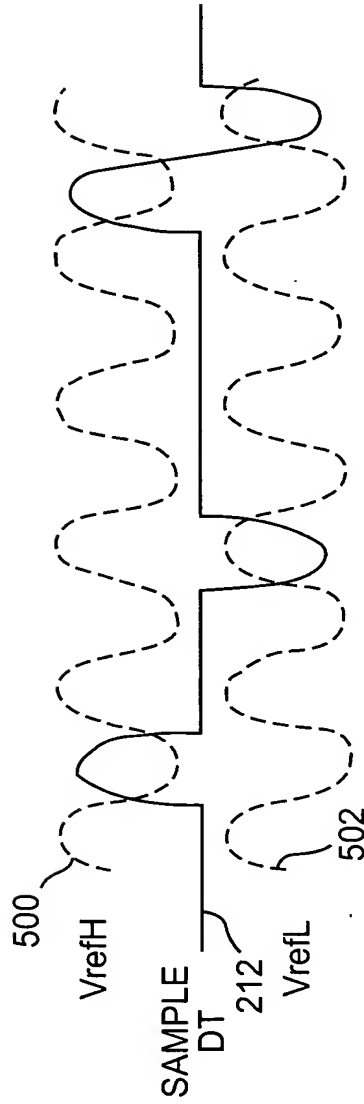


Fig. 6